

ABSTRACT

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2 A programmable after-package, on-chip reference voltage trim circuit for an
3 integrated circuit having a plurality of programmable trim cells generating a programmed
4 sequence. A converter is provided to convert the bit sequence into a trim current. The
5 trim current is added to an initial value of a reference voltage to be trimmed, as generated
6 by the integrated circuit. Once the correct value of the trim current is determined,
7 isolation circuitry is programmed to isolate the trim circuitry from the remainder of the
8 IC, thereby freeing the logic and package pins associated with the IC for use by users of
9 the IC. The preferred trim circuitry includes fuses which are blown in accordance with a
10 bit value supplied to the trim cell to permanently fix a trim current value, once a best fit
11 value is determined.